

Notice of References Cited	Application/Control No. 10/656,986	Applicant(s)/Patent Under Reexamination LU ET AL.	
	Examiner Heather A. Doty	Art Unit 2813	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,780,574	08-2004	Kawashima, Miyoko	430/394
	C	US-4,596,759	06-1986	Schupp et al.	430/271.1
	D	US-6,593,064	07-2003	Gelbart, Daniel	430/311
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf and R.N. Tauber, Silicon Processing for the VLSI Era, Vol. 1, Lattice Press, 1986, pp. 408 and 420.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.